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O I P Karen Cinq-Mars 10/20/03
(Signature & date)

OCT 22 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : October 20, 2003

Geng Wang, et al. : Group Art Unit:

Serial No. 10/604,731 : Examiner:

Filed: 8/13/03 : International Business Machines Corporation
2070 Route 52
Hopewell Junction, NY 12533

TITLE: SELF-ALIGNED DRAIN/CHANNEL JUNCTION IN VERTICAL PASS TRANSISTOR DRAM CELL DESIGN FOR DEVICE SCALING

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure set forth in 37 C.F.R. 1.56, and further pursuant to the provisions of 37 C.F.R. 1.97 and 1.98, applicants hereby respectfully submit copies of the non-US patents and publications as listed on Form PTO-1449, attached hereto.

In citing these documents, no representation is made nor intended as to the pertinency or non-pertinency of the art, that better art than that listed is not available, or that other art is not applicable.

No fee is believed to be due for this submission. If any fees are required, however, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 09-0458.

Respectfully submitted,
Geng Wang, et al.

By

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